



EXPRESS MAIL LABEL NO. EV669678711US

PATENTS  
Attorney Docket No.: ELM-2 DIV. 6

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Glenn J. Leedy  
Application No. : 10/614,067 Confirmation No.: 8117  
Filed : July 3, 2003  
For : THREE DIMENSIONAL STRUCTURE  
INTEGRATED CIRCUIT  
Group Art Unit : 2822  
Examiner : Pamela E. Perkins

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97,  
applicant wishes to call the attention of the Examiner to the  
following documents:

U.S. Patent Documents

US 5,062,689	11/05/1991	Koehler
US 5,117,282	05/26/1992	Salatino
US 6,017,658	01/25/2000	Rhee et al.

Nonpatent Literature Documents

S. Wolf, Silicon Processing For the VLSI Era,  
1990, Lattice Press, Volume 2, page 191

The aforementioned references are listed on the  
accompanying Form PTO/SB/08 (submitted in duplicate).  
Pursuant to 37 C.F.R. 1.98 (a)(2), a copy of the

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aforementioned Non-Patent Literature Document is enclosed herewith.

Applicant reserves the right to establish the patentability of the claimed invention over any of the information provided herewith, and/or to prove that this information may not be prior art, and/or to prove that this information may not be enabling for the teachings purportedly offered.

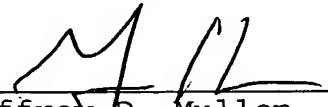
The aforementioned Salatino document was cited in an Office Action mailed in co-pending commonly assigned U.S. Patent Application No. 10/742,057 on May 18, 2006; the Koehler document was cited in an Office Action mailed in co-pending commonly assigned U.S. Patent Application No. 10/385,386 on April 26, 2006; the Rhee et al. document was cited in an Office Action mailed in co-pending commonly assigned U.S. Patent Application No. 10/766,557 on June 5, 2006; the Wolf document was cited in an Office Action mailed in co-pending commonly assigned U.S. Patent Application No. 10/379,820 on April 20, 2006.

It is respectfully requested that these references be: (1) fully considered by the Patent and Trademark Office during the examination of this application; and (2) printed on any patent which may issue on this application. Applicant requests that a copy of Form PTO-SB/08, as considered and initialed by the Examiner, be returned with the next communication.

This Information Disclosure Statement is being submitted after the mailing of a first office action on the merits. Accordingly, the director is authorized to charge \$180.00, in accordance with 37 C.F.R. § 1.97(c) to Account No. 06-1075, Order No. 001202-0132. A duplicate copy of this Statement is enclosed herewith.

An early and favorable action is respectfully  
requested.

Respectfully submitted,



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PTO/SB/08a/b (08-03)

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<b>Substitute for form 1449A/B/PTO</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)				<b>Complete if Known</b>	
				Application Number	10/614,067 (Conf. No. 8117)
				Filing Date	July 3, 2003
				First Named Inventor	Leedy
				Art Unit	2822
				Examiner Name	Pamela Perkins
Sheet	1	of	1	Attorney Docket Number	ELM-2 Div. 6

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
		US 5,062,689	11/05/1991	Koehler	
		US 5,117,282	05/26/1992	Salatino	
		US 6,017,658	01/25/2000	Rhee et al.	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		S. Wolf, Silicon Processing For the VLSI Era, 1990, Lattice Press, Volume 2, page 191.	

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

Examiner Signature		Date Considered	
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